

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Masahiro Higashiguchi et al.

Patent No.: 7,381,066

Issued: June 3, 2008

For: PRINTED CIRCUIT BOARD WITH
INTERCONNECTION TERMINAL, A
PRINTED CIRCUIT ASSEMBLY AND
ELECTRONIC APPARATUS (as amended)

**REQUEST FOR EXPEDITED ISSUANCE OF CERTIFICATE OF CORRECTION
PURSUANT TO 37 C.F.R. § 1.322**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Madam:

Upon reviewing the above-identified patent, Patentee noted a typographical error on the part of the PTO which should be corrected.

On the Cover of Patent:

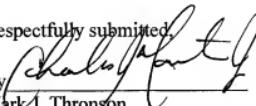
At line 22, change “Sep. 29, 2005” to --Sep. 21, 2005--.

The error was not in the application as filed and was corrected in a Request for Corrected Filing Receipt filed on February 23, 2007. A copy of the corrected Filing Receipt is enclosed. Accordingly, no fee is required.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment.
Patentee respectfully solicits the granting of the requested Certificate of Correction.

Dated: May 12, 2009

Respectfully submitted,

By 
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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 7,381,066

APPLICATION NO. : 10/580,261

ISSUE DATE : June 3, 2008

INVENTOR(S) : Masahiro Higashiguchi et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover of Patent:

At line 22, change "Sep. 29, 2005" to --Sep. 21, 2005--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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| APPL NO. | FILING OR 371(c) DATE | ART UNIT | FIL FEE RECD' | ATTY.DOCKET NO | TOT CLMS | IND CLMS |
|------------|--------------------------|----------|---------------|-----------------|----------|----------|
| 10/580,261 | 05/25/2006 | 2839 | 1700 | R2184.0499/P499 | 24 | 6 |

CONFIRMATION NO. 3236

24998
 DICKSTEIN SHAPIRO LLP
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CORRECTED FILING RECEIPT



OC000000023365292

MJ
 4-17-07 (TCL)

Date Mailed: 04/13/2007

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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 Kazuhiro Yoneda, Osaka, JAPAN;

Power of Attorney: The patent practitioners associated with Customer Number **24998**.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP05/17917 09/21/2005

Foreign Applications

JAPAN 2004-283078 09/29/2004

If Required, Foreign Filing License Granted: 02/02/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/580,261**

Projected Publication Date: 05/17/2007

Non-Publication Request: No

Early Publication Request: No

Title

Printed circuit board, a printed circuit assembly and electronic apparatus

Preliminary Class

439

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